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**B.TECH-II SEM-I I-MIDEXAMINATIONS**

**SET-II**

**Date:01/10/2024 Time:10:00AM to 12:00PM**

**Subject: COA Branch:IT**

**PART-A 5x 2M =10 M**

**BTL CO**

1. Define interrupt and explain the types of interrupts **1 1**
2. Define BUS and explain the types of BUS **1 1**
3. Explain about microinstruction format. **1 2**
4. Difference between instruction format and micro instruction format **2 2**
5. Explain about datatypes **1 3**

**PART-B 4 x 5 M = 20 M**

**BTL CO**

1. Explain about computer functional units with neat diagram? 1 **1**
2. Define micro-operation and explain about micro-operations with examples 1 1
3. Illustrate the control unit methods with neat diagrams **3 2**
4. Explain about control memory with neat diagram? **1 2**
5. Explain about Design of control unit with neat diagram? **1 2**
6. what is meant by fixed-point representation with examples **1 3**

**SCHEME OF EVALUATION**

**Part –A**

| **SNO** | **THEORY** | **MARKS** | **TOTAL** |
| --- | --- | --- | --- |
| **1** | interrupt and types of interrupts | **2** | **2** |
| **2** | BUS and types of BUS | **2** | **2** |
| **3** | Microinstruction format | **2** | **2** |
| **4** | Difference between instruction format and micro instruction format | **2** | **2** |
| **5** | datatypes | **2** | **2** |

**Part –B**

| **SNO** | **THEORY** | **MARKS** | **TOTAL** |
| --- | --- | --- | --- |
| **6** | computer functional units | **5** | **5** |
| **7** | micro-operation with example | **5** | **5** |
| **8** | control unit methods with neat diagrams | **5** | **5** |
| **9** | control memory with neat diagram | **5** | **5** |
| **10** | Design of control unit with neat diagram | **5** | **5** |
| **11** | fixed-point representation with examples | **5** | **5** |